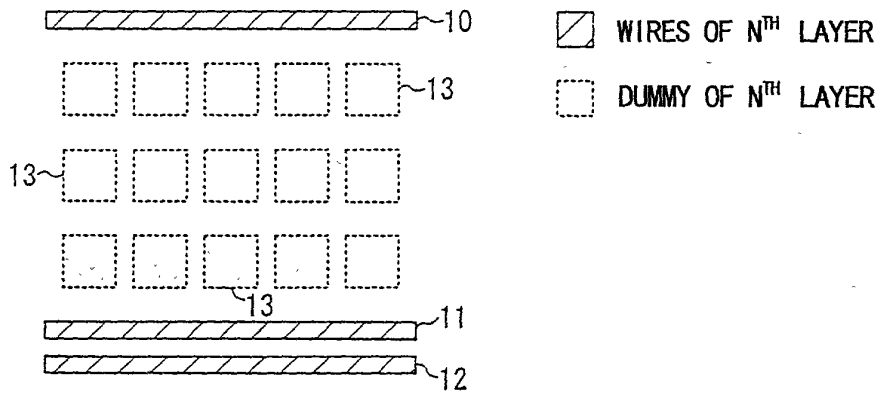
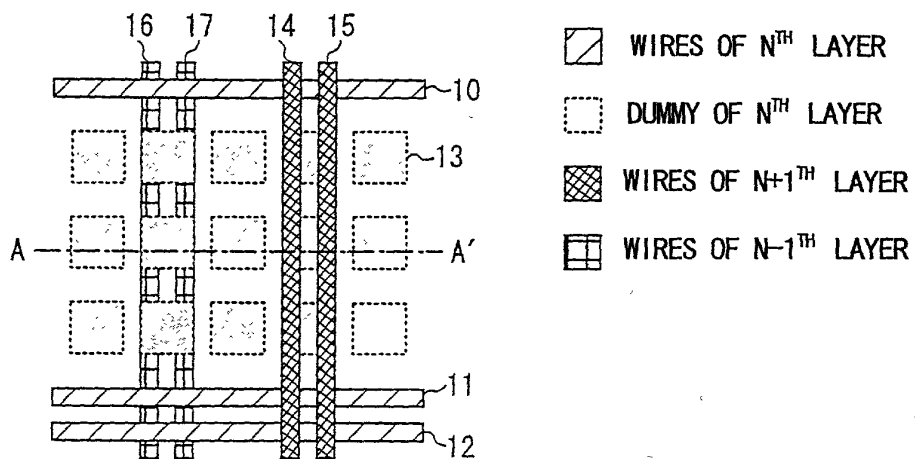


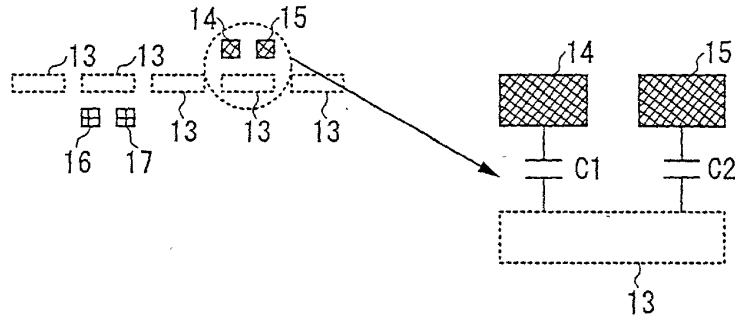
# FIG. 1



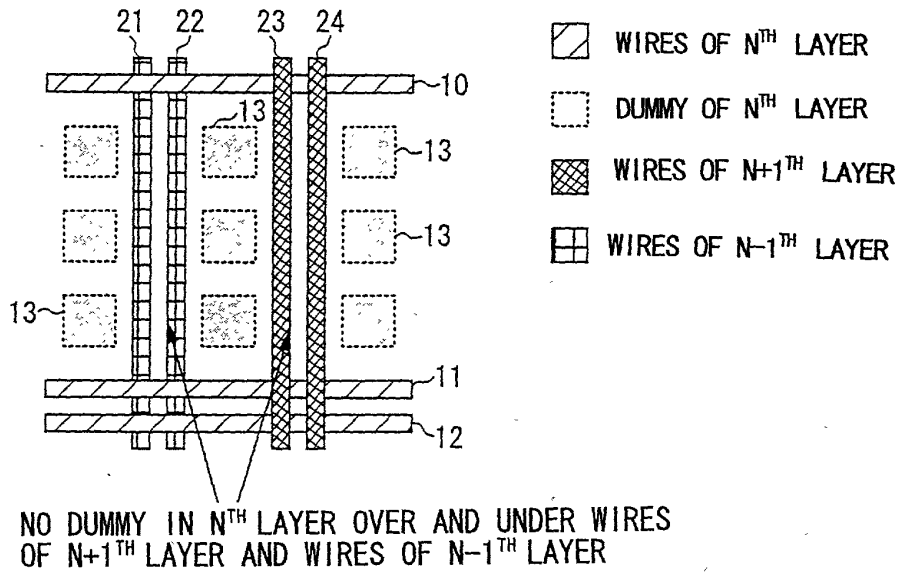
# FIG. 2



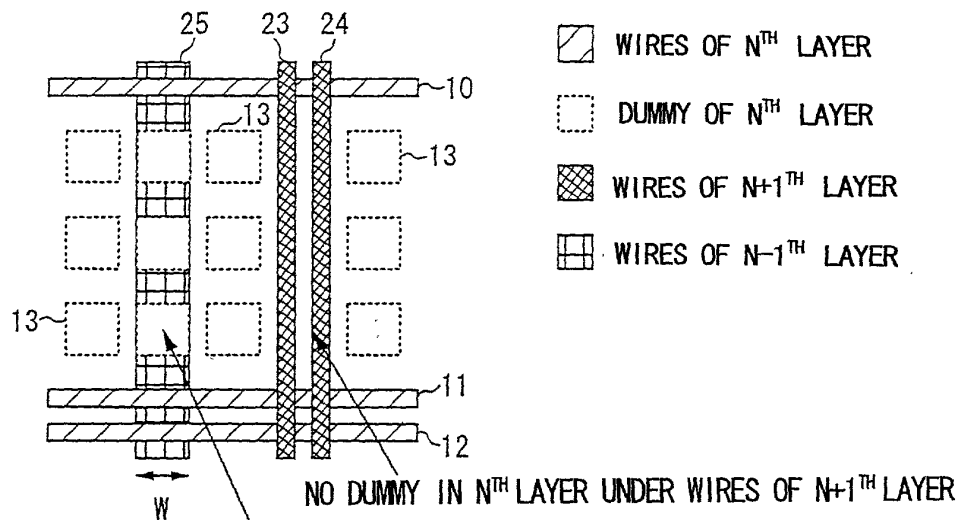
# FIG. 3



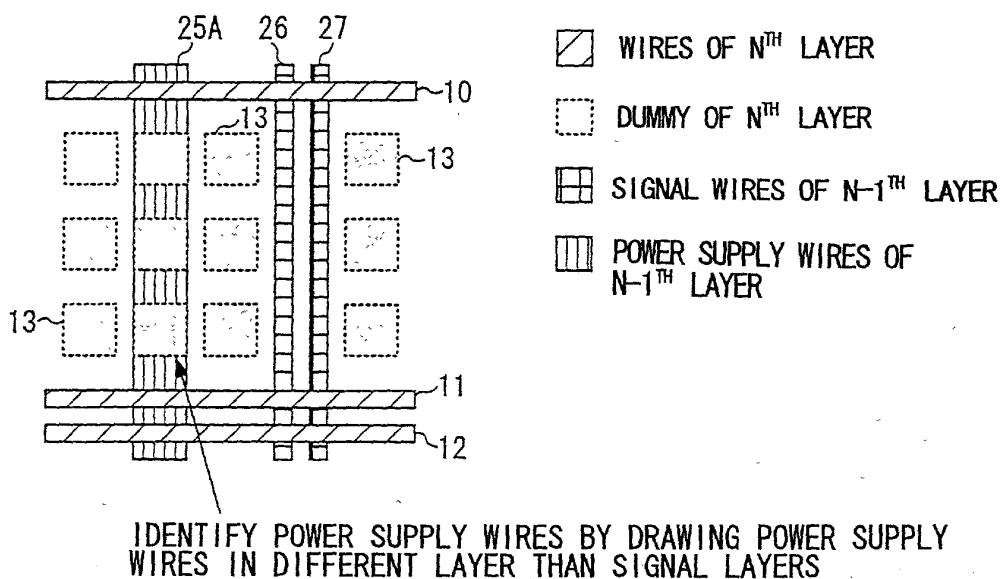
# FIG. 4



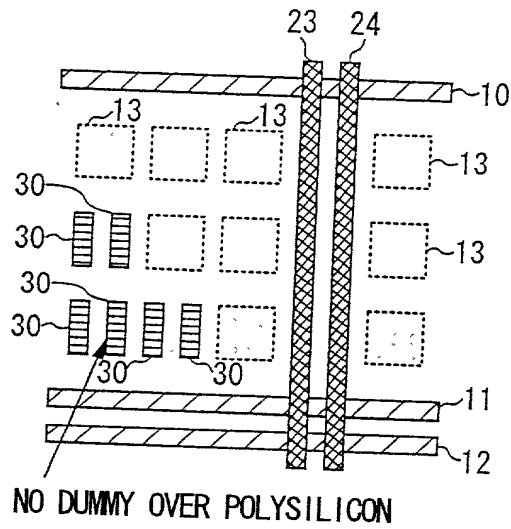
# FIG. 5



# FIG. 6

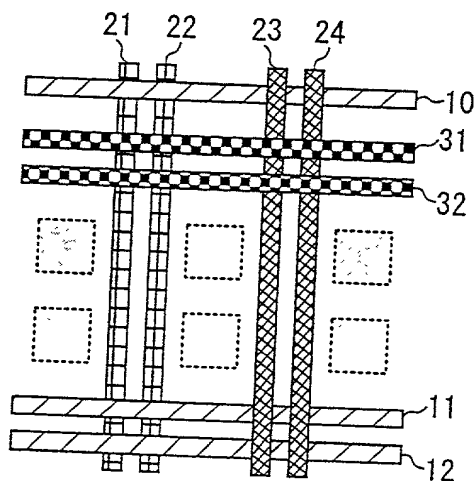


# FIG. 7



- WIRES OF  $N^{\text{TH}}$  LAYER
- DUMMY OF  $N^{\text{TH}}$  LAYER
- WIRES OF  $N+1^{\text{TH}}$  LAYER
- POLYSILICON OR DIFFUSION LAYER ( $N-1^{\text{TH}}$  LAYER)

# FIG. 8



- WIRES OF  $N^{\text{TH}}$  LAYER
- DUMMY OF  $N^{\text{TH}}$  LAYER
- WIRES OF  $N+1^{\text{TH}}$  LAYER
- WIRES OF  $N+M^{\text{TH}}$  LAYER ( $M$  IS POSITIVE INTEGER)
- WIRES OF  $N-1^{\text{TH}}$  LAYER

FIG. 9A

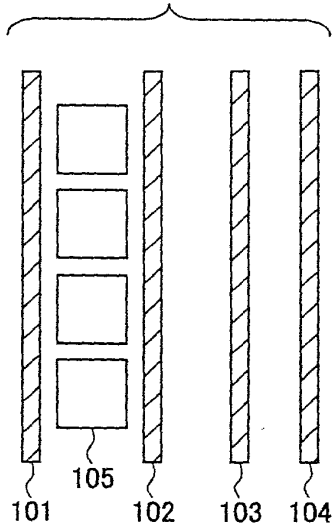


FIG. 9B

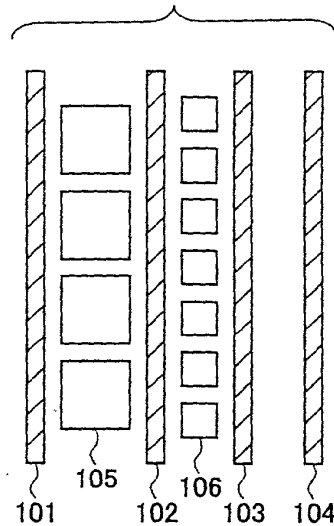


FIG. 9C

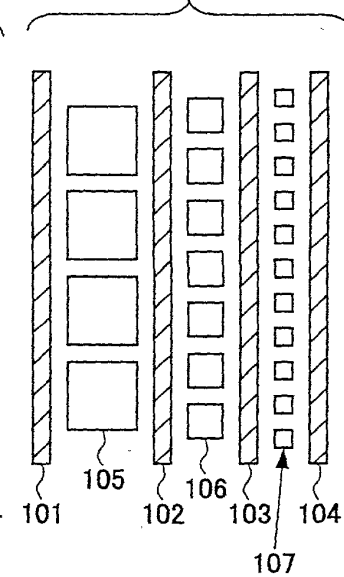
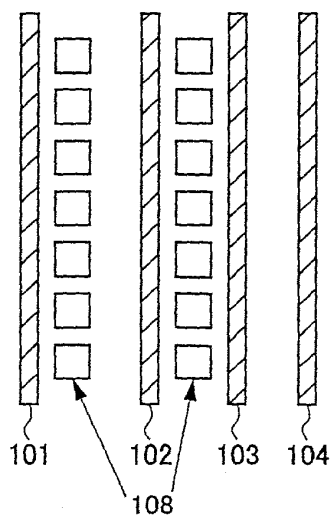


FIG. 10



# FIG.11

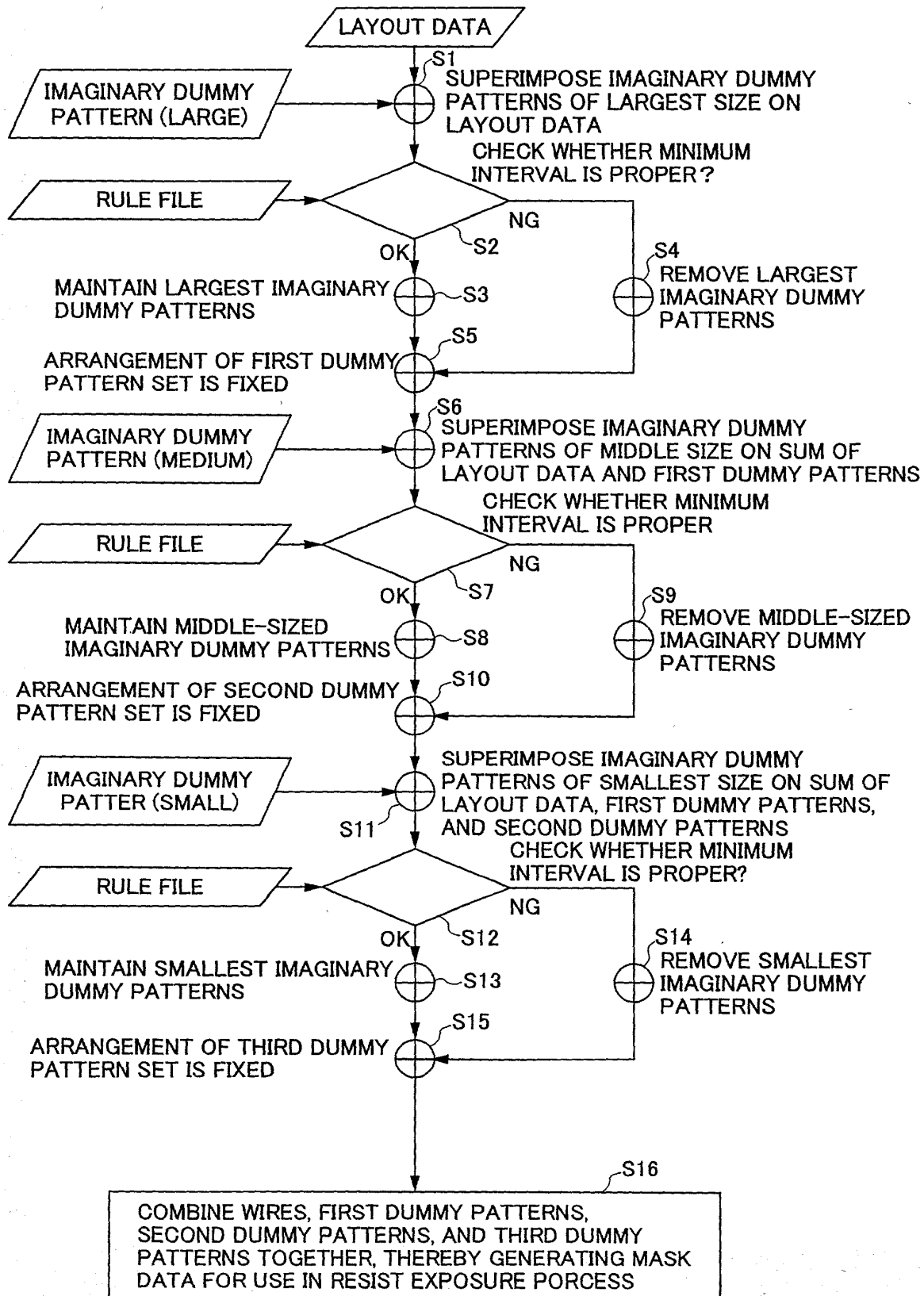


FIG. 12A

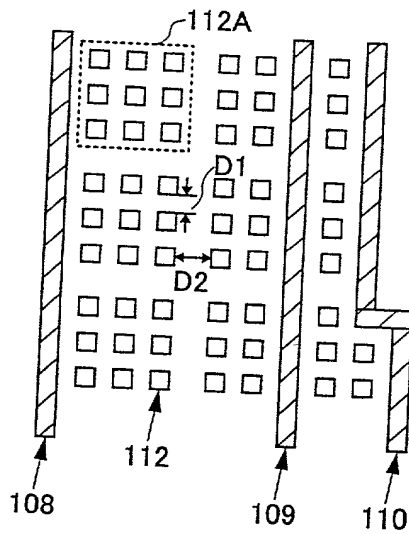


FIG. 12B

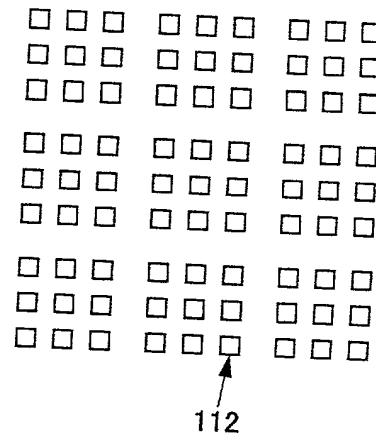
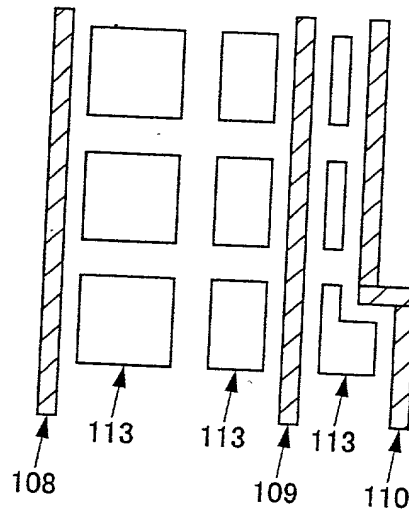


FIG. 12C



# FIG.13

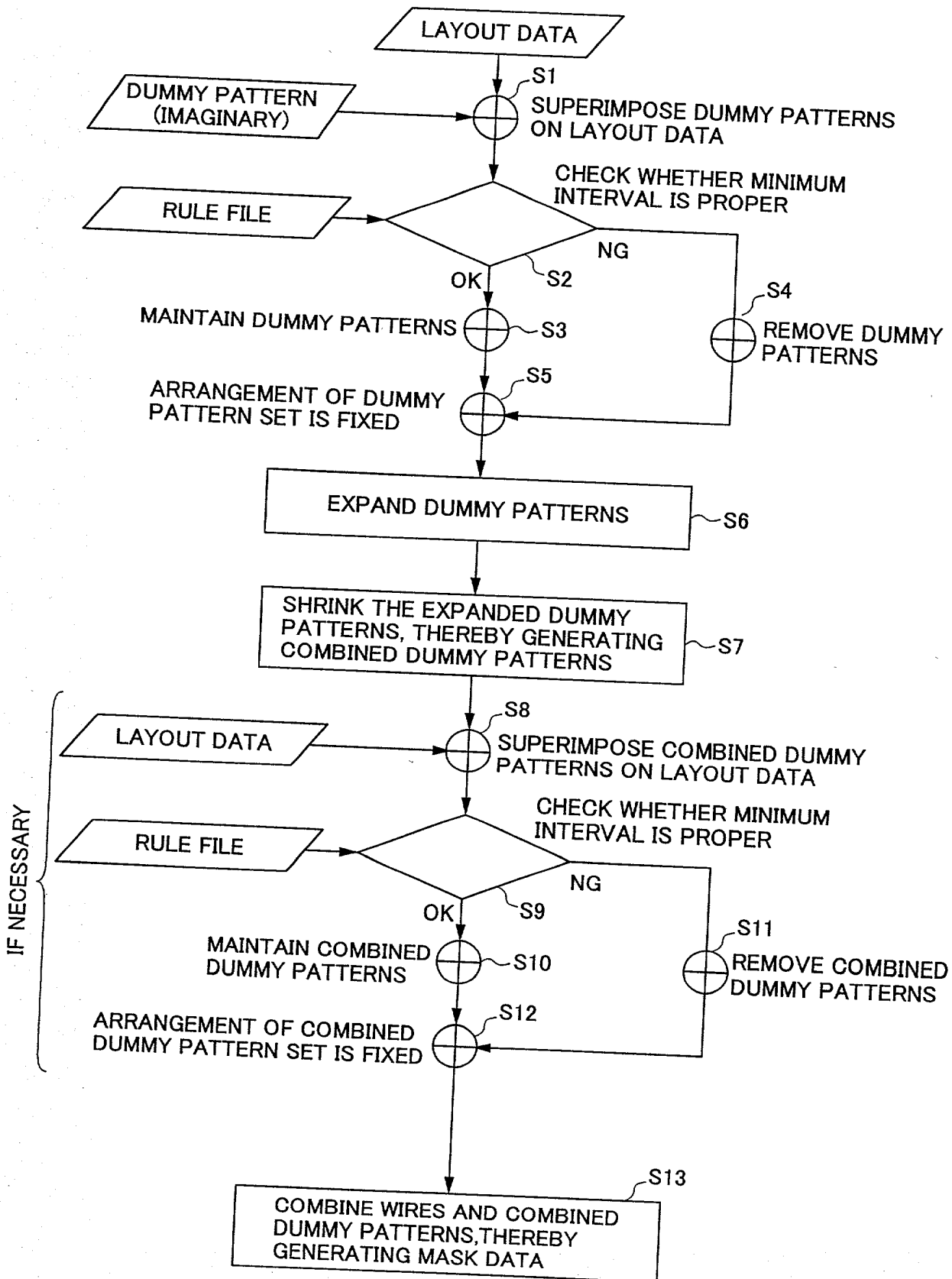




FIG. 14A

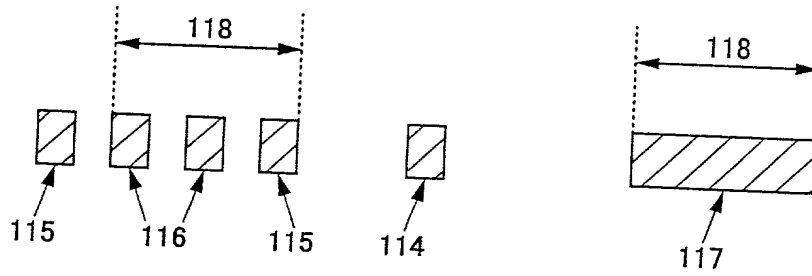
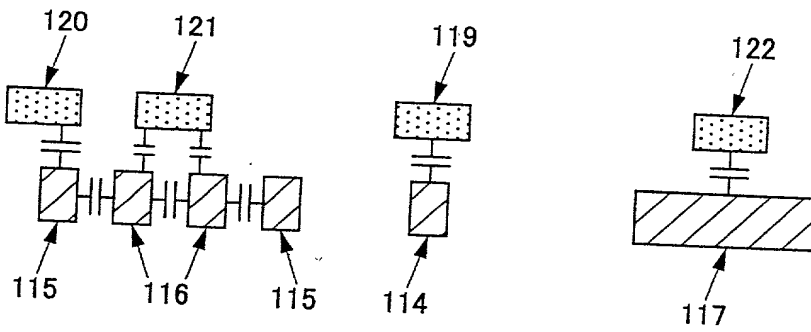


FIG. 14B



# FIG.15

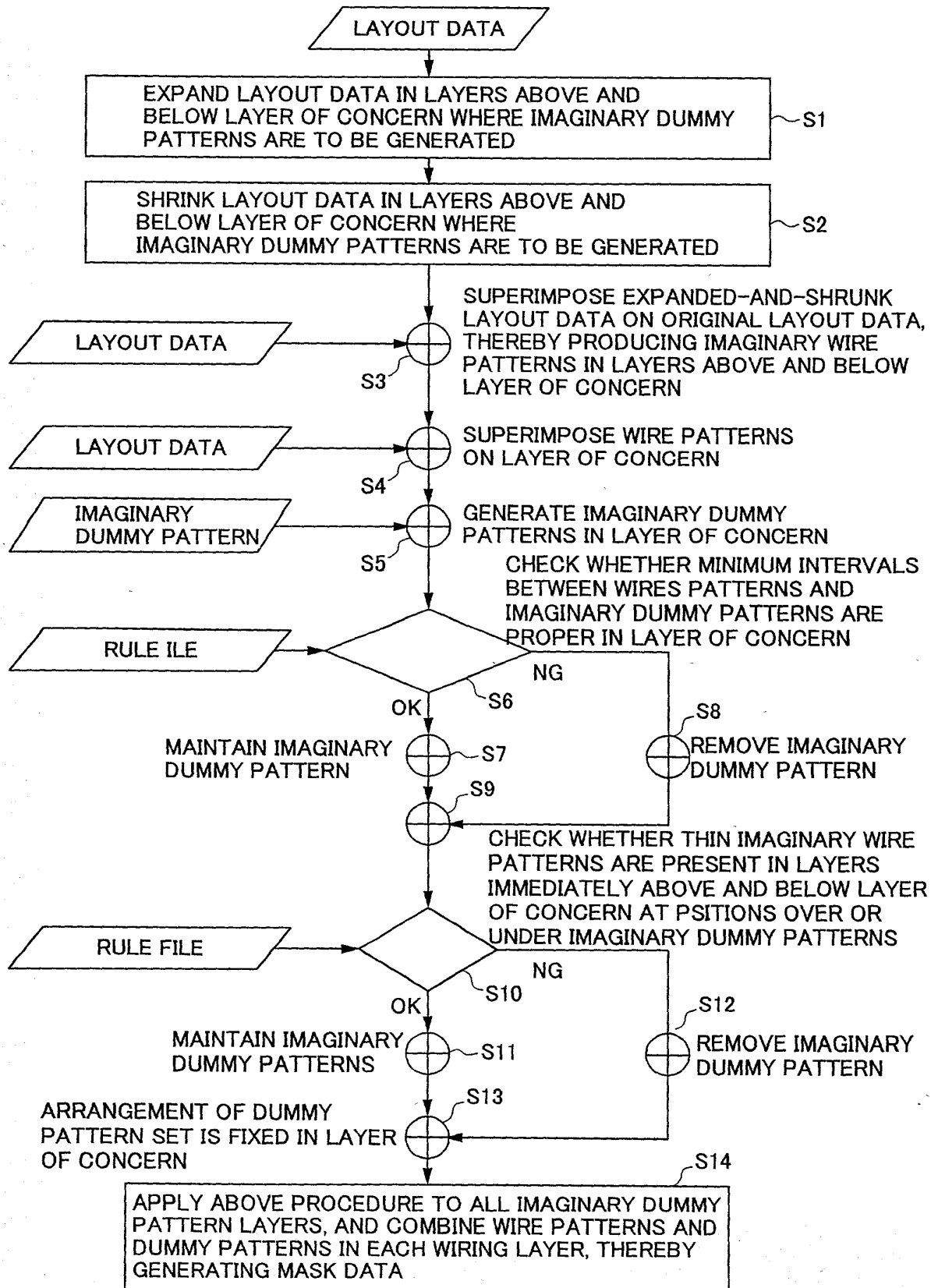


FIG. 16

INITIAL WIRES

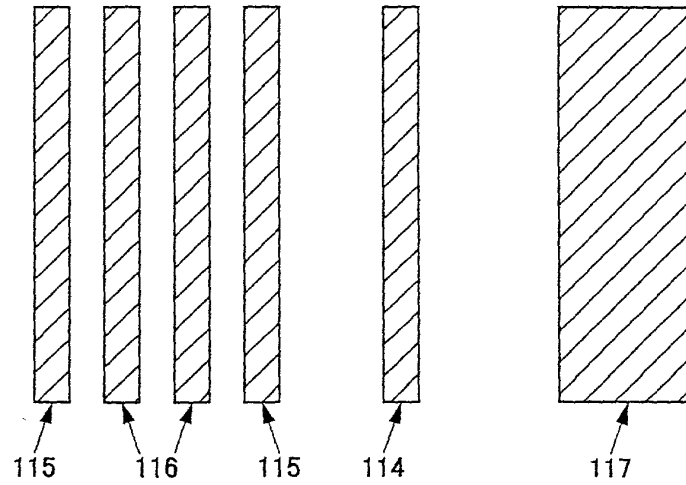
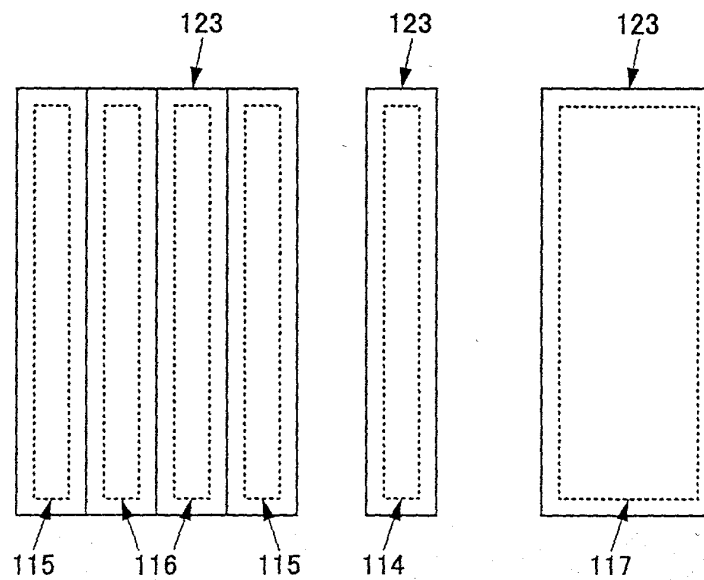


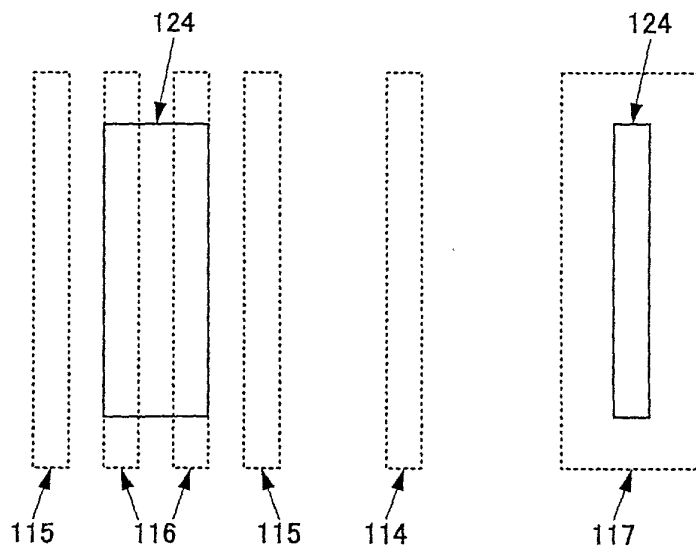
FIG. 17

FIRST PROCESS



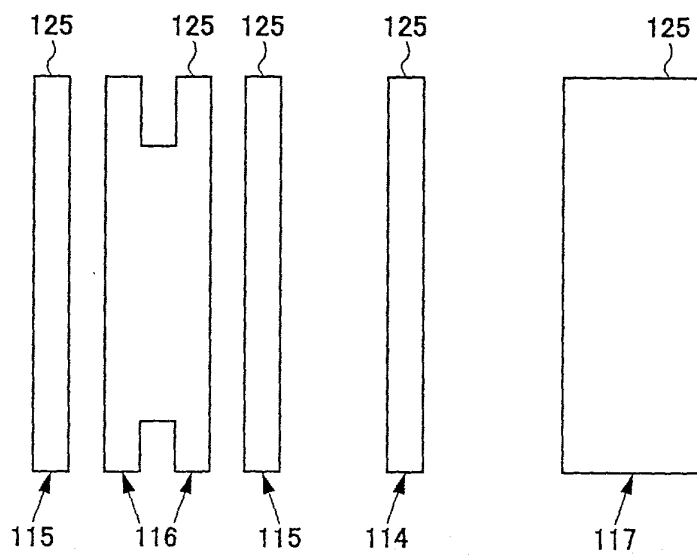
# FIG. 18

SECOND PROCESS



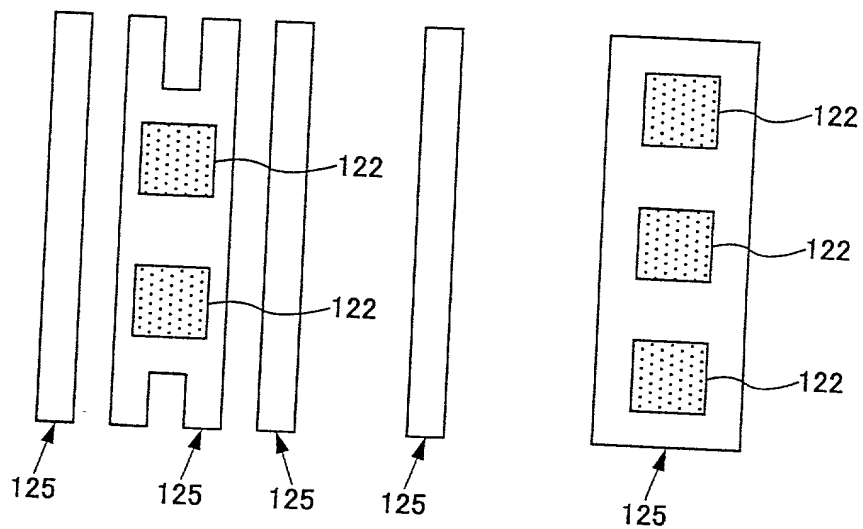
# FIG. 19

THIRD PROCESS



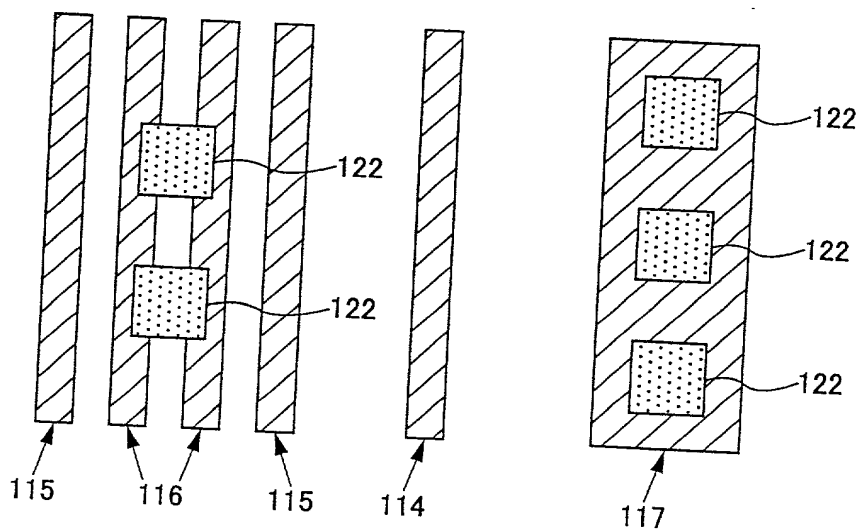
# FIG. 20

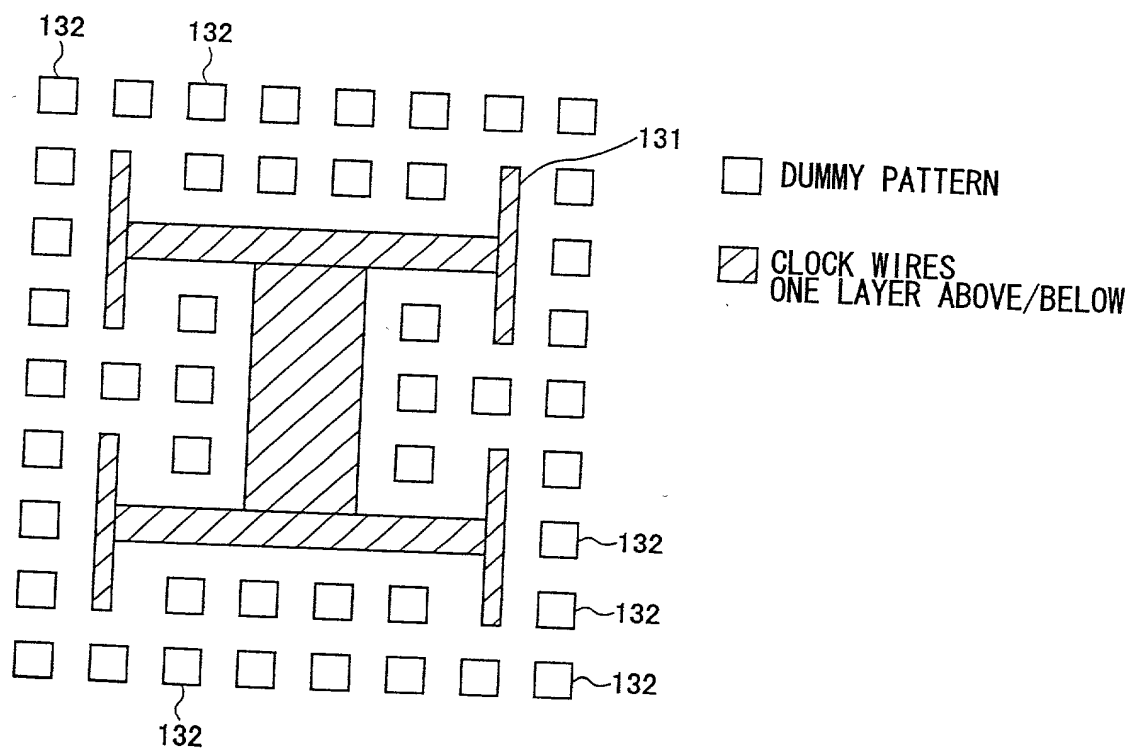
FOURTH PROCESS



# FIG. 21

FIFTH PROCESS



[illegible]

# FIG.23

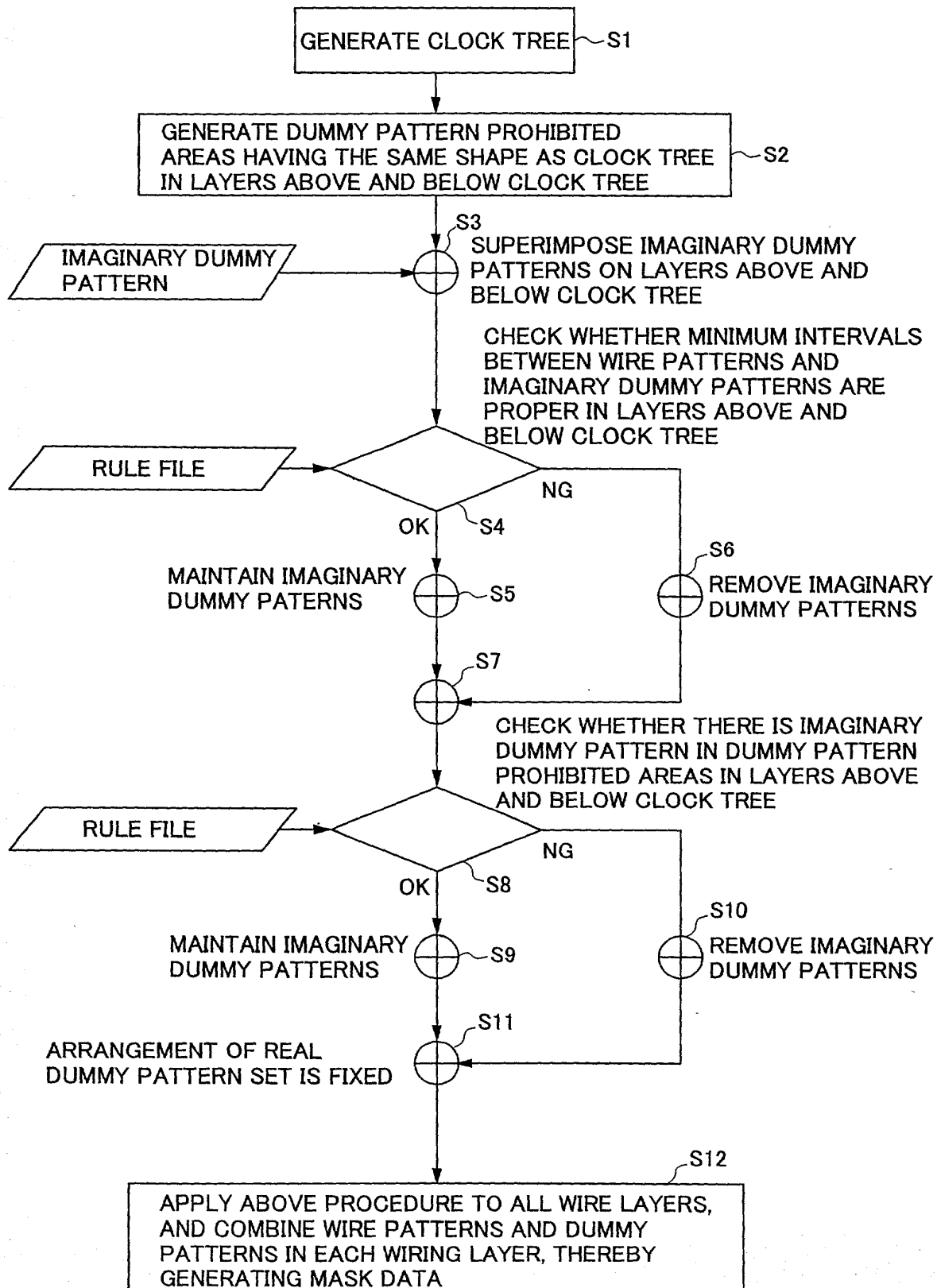


FIG. 24

